

PCIe Connectivity Datasheet

A practical reference for engineers and technical buyers evaluating MCIO, OCuLink, EDSFF and PCIe connectivity components. Use this document to identify a likely configuration, then confirm compatibility for the complete host-to-device path.

Product Families

MCIO cable assemblies PCIe 5.0 and PCIe 6.0 paths for server, NVMe and high-density PCIe routing.	OCuLink cables and adapter cards SFF-8611 and SFF-8612 paths for eGPU, mini PC, NVMe and PCIe expansion.
EDSFF storage connectivity MCIO to EDSFF, E1.S, E1.L, E3.S and E3.L cable and adapter configurations.	Related PCIe interconnects U.2, Slim SAS, Mini SAS and adapter card paths for storage and integration projects.

Selection Path

1	Map the connection	Document the host port, any adapter, cable ends and endpoint device.
2	Confirm electrical requirements	Record the lane count and target PCIe generation for the intended platform.
3	Confirm mechanical fit	Measure the installed routing path, connector direction, clearance and required service length.
4	Prepare the inquiry	Add quantity, country, application and any validation, shielding or packaging requirements.

Quote-ready information

Include in a B2B inquiry Application, connector A and B, lane count, PCIe generation, target length, orientation, platform, quantity, country and validation requirements.
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Reference Configurations

The models below are reference points for a technical discussion. Confirm the complete connection path and platform compatibility before placing a production order.

Model	Reference configuration	Interface	Typical project
LLS-CB-1MC1MC-01	PCIe 5.0 MCIO x8 to MCIO x8 cable	MCIO x8 to MCIO x8	Server, NVMe and high-density PCIe routing
LLS-CB-1MC1MC-03	PCIe Gen6 MCIO x8 to MCIO x8 cable	MCIO x8 to MCIO x8	Gen6 MCIO connectivity review
LLS-CB-1MC1ED-01	PCIe 5.0 MCIO x4 to EDSFF cable with power	MCIO x4 to EDSFF	E1.S, E1.L, E3.S and E3.L NVMe storage
LLS-CB-1MC2ED-01	PCIe 5.0 MCIO x8 to two EDSFF cable	MCIO x8 to 2 EDSFF	Multi-drive enterprise storage
LLS-CB-1OC1OC-01	PCIe 4.0 OCuLink SFF-8611 4i cable	SFF-8611 4i	eGPU, mini PC and NVMe expansion
LLS-CB-1OC1OC-02	PCIe 4.0 OCuLink SFF-8611 8i cable	SFF-8611 8i	Server, storage and PCIe expansion
LLS-CD-1PC1OC-01	PCIe 4.0 x4 to OCuLink adapter card	PCIe x4 to SFF-8612	Internal NVMe and OCuLink expansion

Interface Checklist

MCIO	Confirm x4, x8 or x16 lane configuration, connector direction, PCIe generation, cable route and endpoint interface.
OCuLink	Confirm SFF-8611 cable side or SFF-8612 board side, 4i/8i lanes, host/device direction and installed cable length.
EDSFF	Confirm E1.S, E1.L, E3.S or E3.L form factor, upstream interface, power arrangement and mechanical fit.

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